FIG. 1

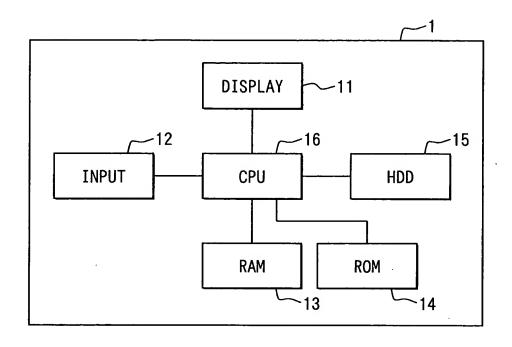


FIG. 2

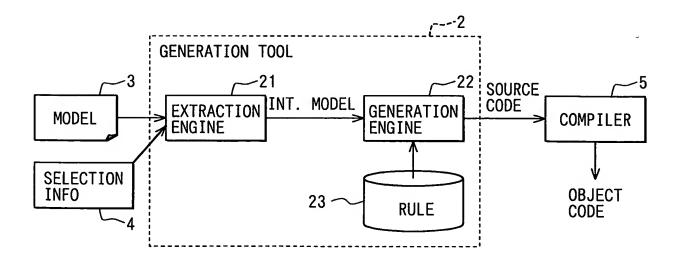


FIG. 3

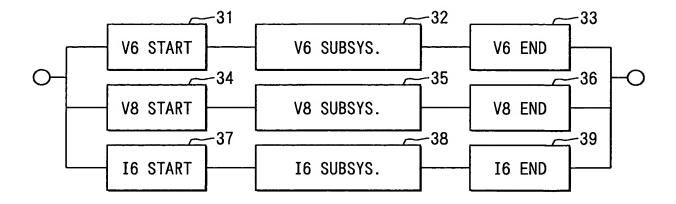


FIG. 4A

V6/I6 SUBSYS.

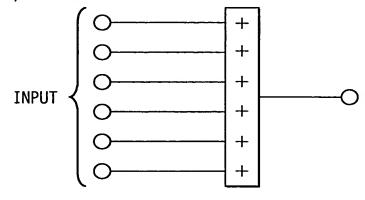


FIG. 4B

V8 SUBSYS.

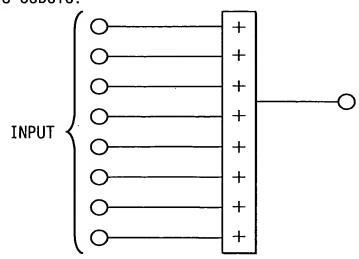


FIG. 5

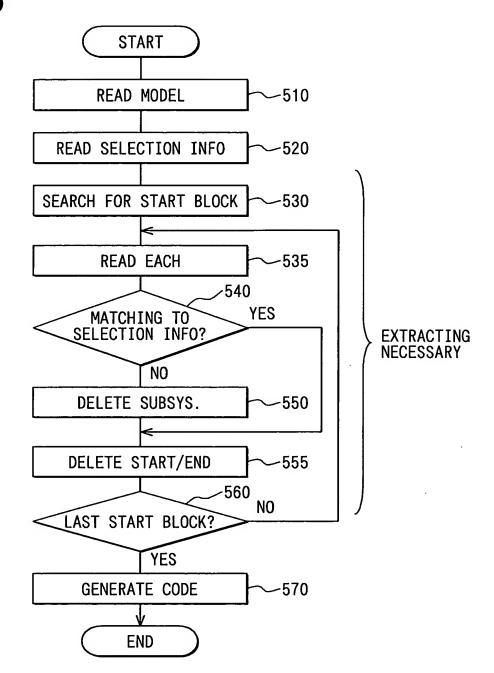


FIG. 6

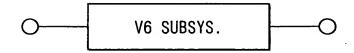


FIG. 7

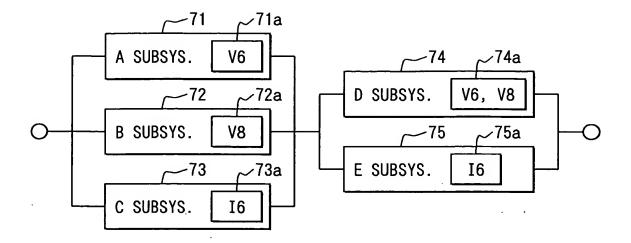


FIG. 8

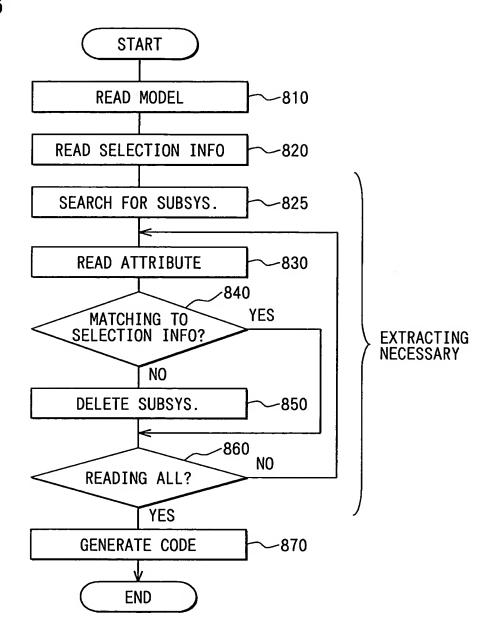


FIG. 9

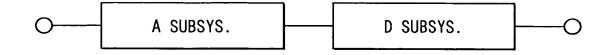


FIG. 10

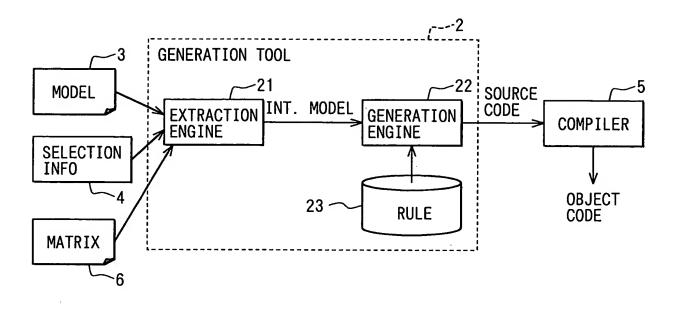


FIG. 11

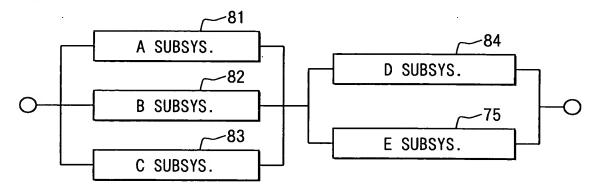


FIG. 12

	V6	V8	16
A SUBSYS.	0		
B SUBSYS.		0	
C SUBSYS.			0
D SUBSYS.	0	0	
E SUBSYS.			0

FIG. 13

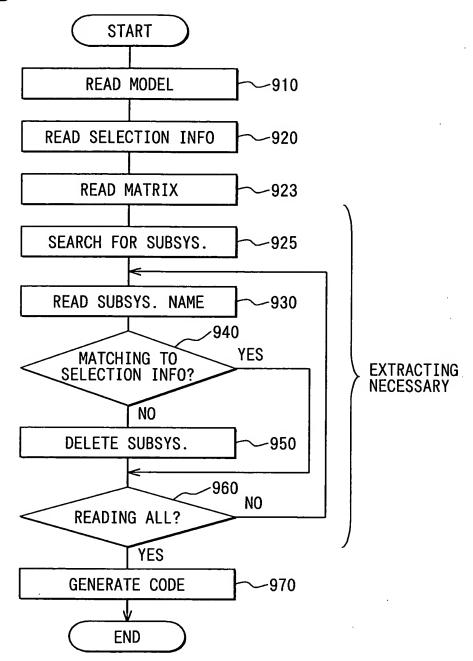


FIG. 14

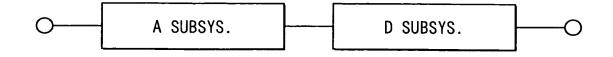


FIG. 15

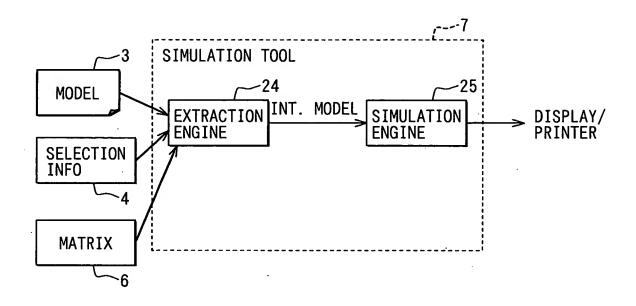


FIG. 16A

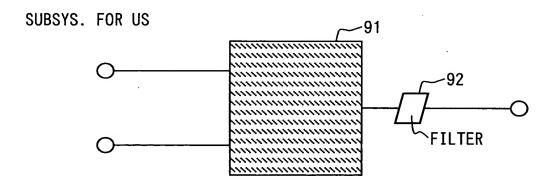


FIG. 16B

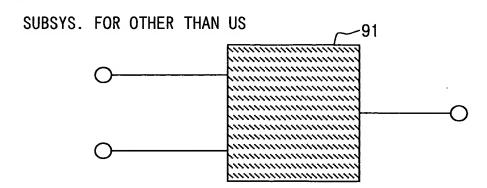


FIG. 17 RELATED ART

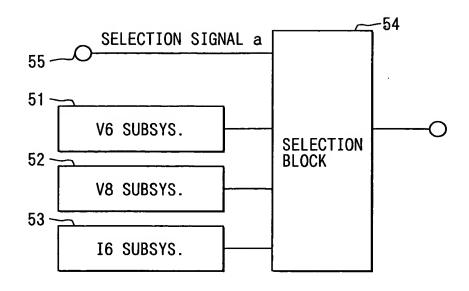


FIG. 18 RELATED ART

```
Switch (a){
  case1:
   V6 SUBSYS.
  break;
  case2:
   V8 SUBSYS.
  break;
  case3:
   I6 SUBSYS.
  break;
}
```